

**6N70****Preliminary****Power MOSFET****6.0A, 700V N-CHANNEL
POWER MOSFET****DESCRIPTION**

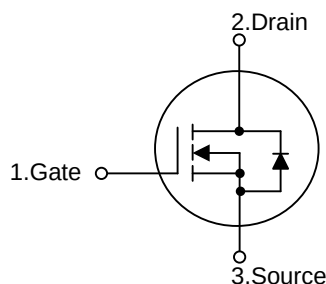
The UTC **6N70** is an N-channel mode power MOSFET using UTC's advanced technology to provide customers with a minimum on-state resistance, high switching speed, low gate charge and low input capacitance.

The UTC **6N70** is universally applied in high efficiency switch mode power supply.

FEATURES

* $R_{DS(ON)}=1.8\Omega$ @ $V_{GS}=10V$, $I_D=3A$

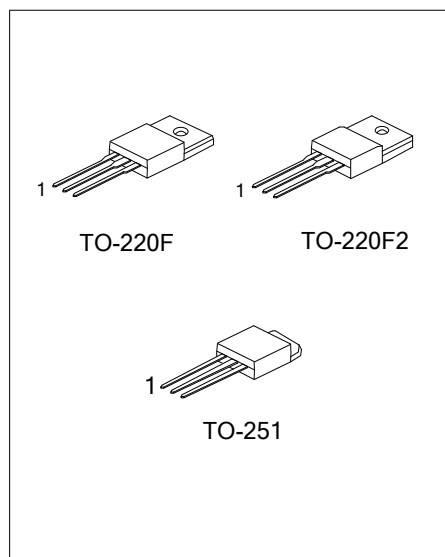
* High switching speed

SYMBOL**ORDERING INFORMATION**

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
6N70L-TF2-T	6N70G-TF2-T	TO-220F2	G	D	S	Tube
6N70L-TF3-T	6N70G-TF3-T	TO-220F	G	D	S	Tube
6N70L-TM3-T	6N70G-TM3-T	TO-251	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

6N70L-TF3-T (1)Packing Type (2)Package Type (3)Lead Free	(1) T: Tube (2) TF2: TO-220F2, TF3: TO-220F, TM3: TO-251 (3) L: Lead Free, G: Halogen Free
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■ ABSOLUTE MAXIMUM RATINGS (unless otherwise specified)

PARAMETER			SYMBOL	RATINGS	UNIT
Drain-Source Voltage			V _{DSS}	700	V
Gate-Source Voltage (Note 2)			V _{GSS}	±30	V
Drain Current	Continuous	T _C =25°C	I _D	6	A
		T _C =100°C		3.8	A
	Pulsed		I _{DM}	24	A
Avalanche Current (Note 2)			I _{AR}	6	A
Avalanche Energy	Single Pulsed (Note 3)		E _{AS}	582	mJ
	Repetitive (Note 2)		E _{AR}	13	mJ
Peak Diode Recovery dv/dt (Note 4)			dv/dt	2.5	V/ns
Power Dissipation		TO-220F2	P _D	42	W
		TO-220F		40	
		TO-251		55	
Linear Derarting Factor		TO-220F2		0.33	W/°C
		TO-220F		0.32	
		TO-251		0.44	
Junction Temperature			T _J	+150	°C
Storage Temperature			T _{STG}	-55~+150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature

3. $L = 30\text{mH}$, $I_{AS} = 6\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 27\Omega$, Starting $T_J = 25^{\circ}\text{C}$

4. $I_{SD} \leq 6\text{A}$, $di/dt \leq 140\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^{\circ}\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220F	θ_{JA}	62.5	$^{\circ}\text{C}/\text{W}$
	TO-220F2			$^{\circ}\text{C}/\text{W}$
	TO-251		110	$^{\circ}\text{C}/\text{W}$
Junction to Case	TO-220F2	θ_{JC}	2.9	$^{\circ}\text{C}/\text{W}$
	TO-220F		3.1	$^{\circ}\text{C}/\text{W}$
	TO-251		2.27	$^{\circ}\text{C}/\text{W}$

■ ELECTRICAL CHARACTERISTICS ($T_C=25^\circ\text{C}$, unless otherwise specified)

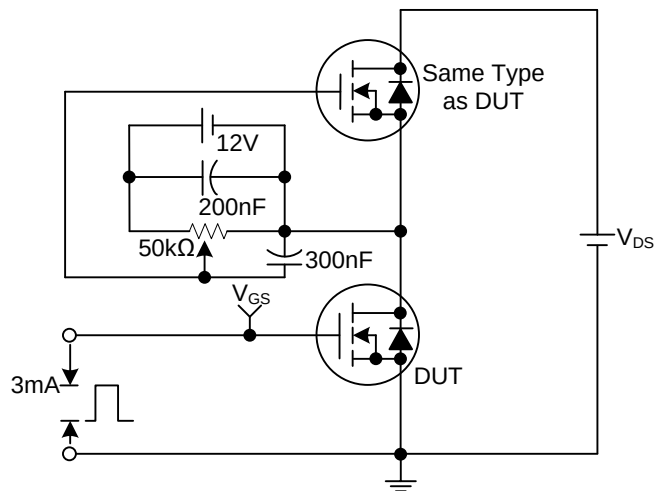
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV_{DSS}	$I_D=250\mu A, V_{GS}=0V$	700			V
Breakdown Voltage Temperature Coefficient		$\Delta BV_{DSS}/\Delta T_J$	$I_D=250\mu A$		0.79		V/°C
Drain-Source Leakage Current		I_{DSS}	$V_{DS}=700V$			25	μA
			$V_{DS}=560V, T_C=125^{\circ}C$			250	μA
Gate-Source Leakage Current	Forward	I_{GSS}	$V_{GS}=+30V, V_{DS}=0V$			+100	nA
	Reverse		$V_{GS}=-30V, V_{DS}=0V$			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=250\mu A, V_{DS}=5V$	2.0		4.0	V
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	$V_{GS}=10V, I_D=3A$ (Note 1)		1.8	1.8	Ω
DYNAMIC PARAMETERS							
Input Capacitance		C_{ISS}	$V_{GS}=0V, V_{DS}=25V, f=1.0MHz$ (Note 1, 2)		900	1200	pF
Output Capacitance		C_{OSS}			90	115	pF
Reverse Transfer Capacitance		C_{RSS}			18	55	pF
SWITCHING PARAMETERS							
Turn-ON Delay Time		$t_{D(ON)}$	$V_{DD}=350V, I_D=6A, R_G=11.5\Omega$		40	70	ns
Rise Time		t_R			65	90	ns
Turn-OFF Delay Time		$t_{D(OFF)}$			190	230	ns
Fall-Time		t_F			88	116	ns
Total Gate Charge		Q_G	$V_{GS}=10V, V_{DS}=560V, I_D=6A$ (Note 1, 2)		110	140	nC
Gate to Source Charge		Q_{GS}			9		nC
Gate to Drain Charge		Q_{GD}			23.1		nC
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I_S	Integral reverse pn-diode in the MOSFET			6	A
Maximum Body-Diode Pulsed Current (Note 3)		I_{SM}				24	A
Drain-Source Diode Forward Voltage (Note 2)		V_{SD}	$I_S=6A, V_{GS}=0V, T_J = 25^{\circ}C$			1.4	V
Body Diode Reverse Recovery Time		t_{rr}	$I_F=6A, dI_F/dt=100A/\mu s, T_J = 25^{\circ}C$		440		ns
Body Diode Reverse Recovery Charge		Q_{RR}	$T_J = 25^{\circ}C$		4.05		μC

Notes: 1. Pulse Test: Pulse width $\leq 250\mu\text{s}$, Duty cycle $\leq 2\%$

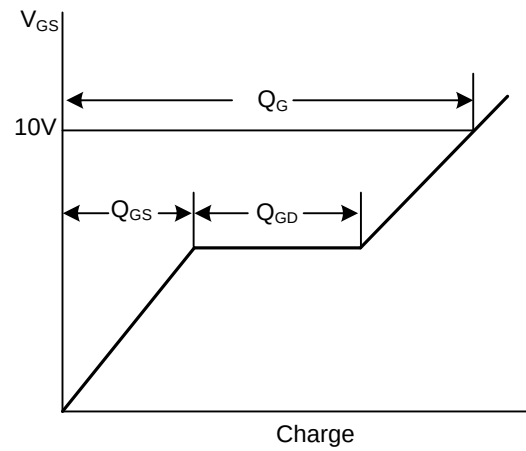
2. Essentially independent of operating temperature

3. Repetitive Rating: Pulse width limited by maximum junction temperature

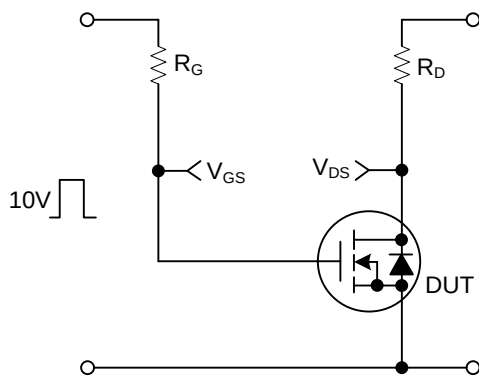
■ TEST CIRCUITS AND WAVEFORMS



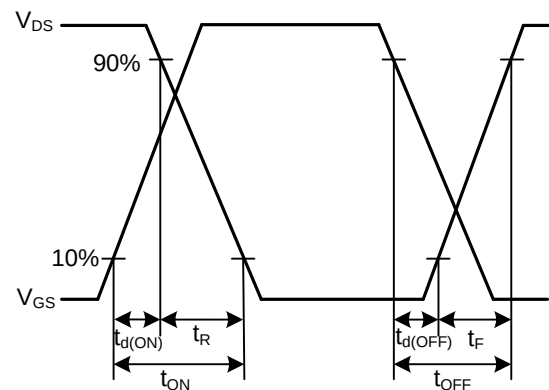
Gate Charge Test Circuit



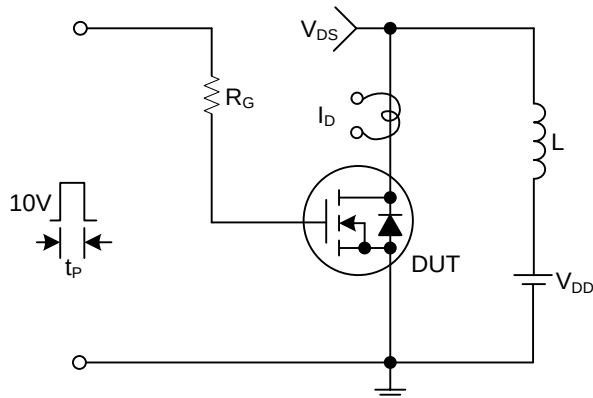
Gate Charge Waveforms



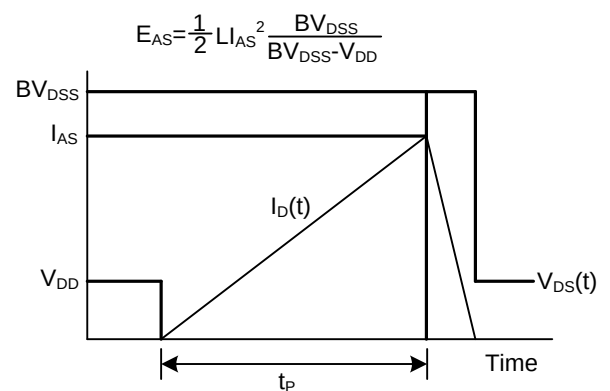
Resistive Switching Test Circuit



Resistive Switching Waveforms

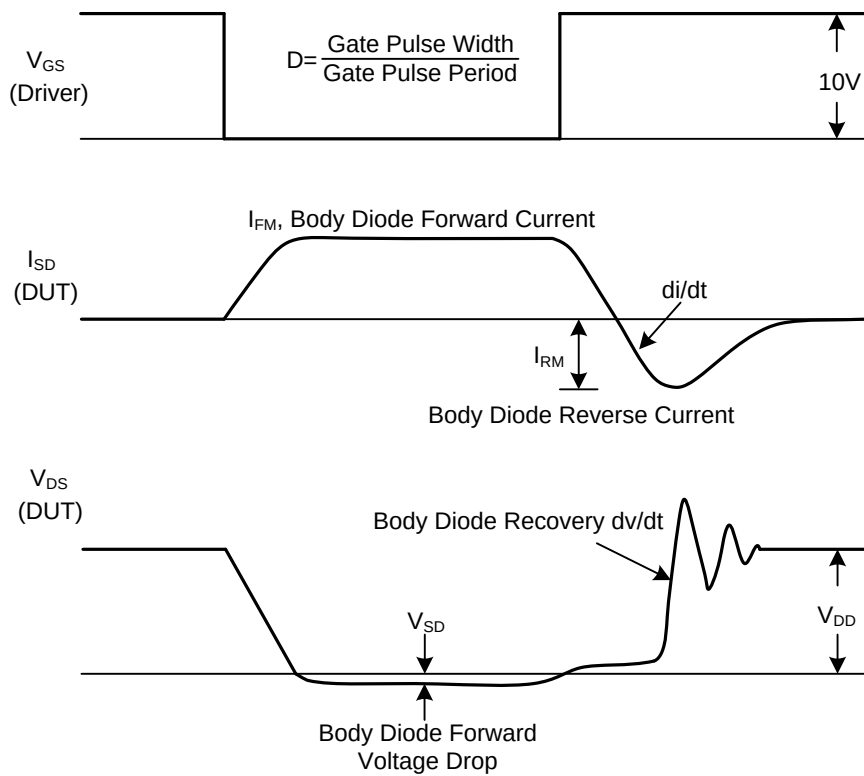
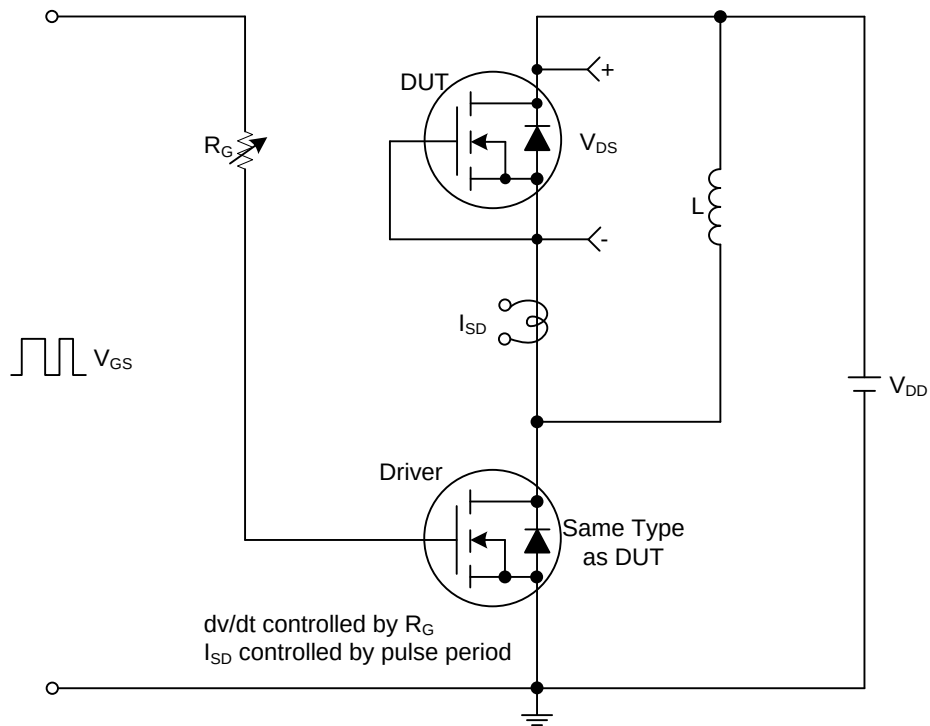


Unclamped Inductive Switching Test Circuit



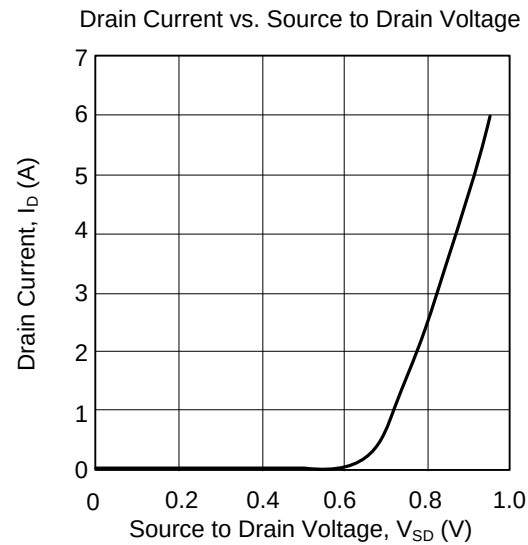
Unclamped Inductive Switching Waveforms

■ TEST CIRCUITS AND WAVEFORMS(Cont.)



Peak Diode Recovery dv/dt Test Circuit and Waveforms

■ TYPICAL CHARACTERISTICS



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