ST1151A

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Low-saturation, Low-voltage 1.5 Channel Bi-directional Motor Driver





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Low-saturation, Low-voltage 1.5 Channel Bi-directional Motor Driver

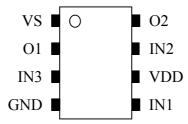
General Specifications

The device is a 1.5-channel low-saturation bi-directional motor driver IC. The design is optimal for motor applications, such as cameras, printers, FDDs, or other portable devices with forward, reverse, bake and stand-by function.

Features and Benefits

- Low voltage operation ($V_{DD \, Min} = V_{SMin} = 1.5V$)
- Low saturation voltage (Upper transistor + low transistor residual voltage; 0.4V typ. at 300mA, $V_{DD} = V_S = 3V$)
- Low input current
- Brake function
- High output sinking and driving capability
- Thin, highly reliable package (SOP-8)

Pin Assignment



Pin NO.	Pin Name	Description	
1	VS	Power supply pin for output driver	
2	01	Output sinking / driving pin	
3	IN3	Input pin 3 that determines driving mode	
4	GND	Ground pin	
5	IN1	Input pin 1 that determines driving mode	
6	VDD	Power supply pin for controller.	
7	IN2	Input pin 2 that determines driving mode	
8	O2	Output sinking / driving pin	



Absolute Maximum Ratings (Unless otherwise noted, T_A = 25 $^{\circ}$ C)

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Symbol	Rating	Unit
V_{DD}	5.5	V
V _S	5.5	V
V _{IN}	V _{DD} +0.4	V
I _{OPeak}	2	Α
I _{ODC}	0.75	Α
P_{D}	680	mW
T _{OPR}	-40 ~ 125	°C
T _{STG}	-65 ~ 150	°C
	V _{DD} V _S V _{IN} I _{OPeak} I _{ODC} P _D T _{OPR}	V _{DD} 5.5 V _S 5.5 V _{IN} V _{DD} +0.4 I _{OPeak} 2 I _{ODC} 0.75 P _D 680 T _{OPR} -40 ~ 125

Electrical Characteristic

(Unless otherwise noted, T_A = 25 % & V_{DD} = V_S = 3V)

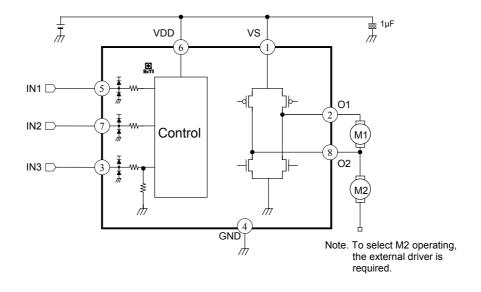
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Characteristic	Sym.	Condition	Limit			Linit
Characteristic			Min.	Тур.	Max.	Unit
Supply Voltage	V_{DD}		1.5	3	5.5	V
Supply Voltage	Vs		1.5	3	5.5	V
Supply Current	I _{DD0}	$V_{IN1, IN2, IN3} = 0V$		0.5	10	μ A
(I _{DD} + I _S)	I _{DD1}	V _{IN1, IN2, IN3} = 3V		1	10	μ A
IN1 / IN2 / IN3 Input Terminal (T _J = 25℃)						
Input Voltage "H"	V _{IH}	-	0.8*V _{DD}	-	V _{DD} +0.	V
Input Voltage "L"	V _{IL}	-	-0.4	-	0.2*V _{DD}	V
Input Current "H"	I _{IH}	$V_{IN} = V_{DD}$	-	-	±5	μ A
Input Current "L"	I _{IL}	V _{IN} = 0 V	-	-	±5	μ A
O1 / O2 Output Terminal (T _J = 25℃)						
Output Voltage	V _{OUT1}	I _{OUT} = 200 mA	-	0.3	0.45	V
Output Voltage (upper + lower)	V _{OUT2}	I _{OUT} = 300 mA	-	0.5	0.7	V
(upper - lower)	V _{OUT3}	I _{OUT} = 600 mA	-	0.9	1.0	V
Output Sustaining Voltage	V _{O(SUS)}	I _{OUT} = 400 mA	-	-	Vs	V



Truth Table

Input Signal			Output Driver		Mode	
IN1	IN2	IN3	01	O2	Mode	
Н	L	L	Н	L	M1Forward	
L	Н	L	L	Н	M1Reverse	
Н	Н	L	Н	Н	M1Brake	
L	L	L	OFF	OFF	Standby	
Н	L	Η	OFF	Н	M2 Operation	
L	Н	Н	OFF	L	M2 Operation	
Н	Н	Н	OFF	Н	M2 Operation	

Block Diagram & Application Circuit

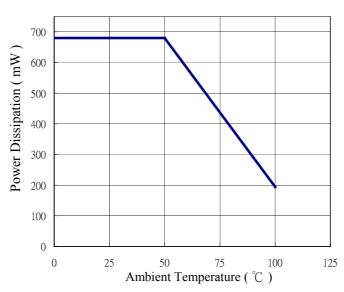




Application Notes

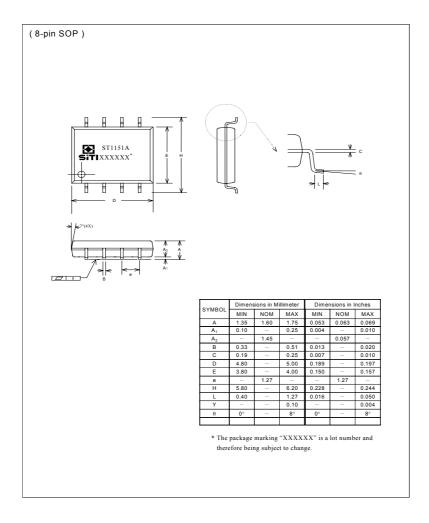
- ☐ In multiple power supply application, the voltage of VDD pin must lager than or equal to the voltage of VS pin.
- □ The power dissipated by the IC varies widely with the supply voltage, the output current, and loading. It is important to ensure the application does not exceed the allowable power dissipation of the IC package. The recommended motor driver power dissipation versus temperature is depicted as follows:







Package Specifications (SOP-8)



The products listed herein are designed for ordinary electronic applications, such as electrical appliances, audio-visual equipment, communications devices and so on. Hence, it is advisable that the devices should not be used in medical instruments, surgical implants, aerospace machinery, nuclear power control systems, disaster/crime-prevention equipment and the like. Misusing those products may directly or indirectly endanger human life, or cause injury and property loss.

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